

LISTA DE LUCRĂRI

S.I. dr. ing. ALIN-GHEORGHITA MAZARE, Universitatea din Pitești

1. Lista celor 10 lucrări relevante pentru realizările profesionale proprii, incluse în dosar:

Laurentiu Ionescu, Alin Mazare, Gabriel Iana, Gheorghe Serban, Ionel Bostan, „Intrinsic Evolvable Hardware Structures”, Chapter 11 of book „Soft Computing: Methods for Practical Environment Solutions”, Ed. Marcos Gestal Pose & Daniel Rivero Cebrian, ISBN 978-1-61520-893-7, p. 160-186, IGI Global, 2010

Gheorghe Serban, Constantin Anton, Ion Tutanescu, Laurentiu Ionescu, Alin Mazare, „An implementation of BCH codes in FPGA”, Proceedings of International Conference of Applied Electronics, IEEE Catalog Number: CFP1069A-PRT, ISBN 978-80-7043-865-7, ISSN 1803-7232, Pilsen, 8-9 September, 2010, p.1-4

Ionescu, A. G. Mazare, G. Serban, „VLSI implementation of an associative content addressable memory based on Hopfield network model”, Proceedings of International Semiconductor Conference CAS2010, 33rd edition, IEEE Catalog number: CFP10CAS-PRT, ISBN: 978-1-4244-5781-6; ISSN: 1545-827X, Vol. 1, Section IC&D, pp 499-502, October 11-13 2010.

Ionescu L., Mazare A., Serban G., Barbu V., Anton C., „FPGA implementation of an associative content addressable memory”, Proceeding of International Conference on Applied Electronics, ISBN 978-80-7043-987-6, ISSN 1803-7232, IEEE Catalog Number CFP1169A-PRT, p. 181-184, Pilsen, 7-8 september 2011

Mazare A., Ionescu L., Serban G., Barbu V., “Evolvable Hardware with Boolean Functions Network Implementation”, Proceeding of International Conference on Applied Electronics, ISBN 978-80-7043-987-6, ISSN 1803-7232, IEEE Catalog Number CFP1169A-PRT, p. 255-260, Pilsen, 7-8 september 2011

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G. Șerban, C. Anton, L. Ionescu, I. Tutănescu, A. Mazăre – “H-ARQ System for real time Error Detection and Correction based on Hardware Associative Memories”, International Conference on Applied Electronics AE'2012, 5-7 September, 2012, Pilzen, Czech Republic, lucrare publicata in Conference Proceedings ISSN 1803-7232

Gheorghe Șerban, Laurențiu Ionescu, Alin Mazăre, „The possibility of optimisation for power supply consumption using evolvable power regulator”, Revue Roumaine des Sciences Techniques, Serie Electrotechnique et Energetique, Tome 57, 2012, No 2, Avril-Juin, pp.222-231, ISSN: 0035-4066

A Mazare, L Ionescu, G Serban, I Lita, “FPGA-based system for data acquisition and remote communication”, Design and Technology in Electronic Packaging (SIITME), 2014 IEEE 20th International Symposium for, 169-174, DOI: 10.1109/SIITME.2014.6967019, 2014

LM Ionescu, A Mazare, C Anton, C Stoian, “The hardware implementation of a quantum computation system emulator”, Applied Electronics (AE), 2014 International Conference on, ISSN 1803-7232, ISBN 978-8-0261-0276-2, 121 – 124, 2014

2. Teza de doctorat

“ Modelarea, simularea și implementarea de structuri hardware reconfigurabile bazate pe concepte din inteligența artificială”, 2007

3. Brevete de invenții și alte titluri de proprietate industrială

Ionita S., Mazare A.G., Ionescu L., Angheliescu P. „system with programmable repeater radio-relay for in IR remote control includes a radio-transmitter which sends encoded numerical commands to a central control unit, a radio relay comprising a radio receiver module”, Patent number RO126719 – A0

4. Cărți și capitole din cărți

Laurentiu Ionescu, Emil Sofron, Gheorghe Serban, Alin Mazare – “Evolved synthesis of digital circuit”, Encyclopedia of Artificial Intelligence, Edited by: Juan Rabunal, Alejandro Pazos Sierra, University of A Coruna, IGI Global, 2007, Spain, ISBN 978-1-59904-849-9

Laurentiu Ionescu, Alin Mazare, Gabriel Iana, Gheorghe Serban, Ionel Bostan, „Intrinsic Evolvable Hardware Structures”, Chapter 11 of book „Soft Computing: Methods for Practical Environment Solutions”, Ed. Marcos Gestal Pose & Daniel Rivero Cebrian, ISBN 978-1-61520-893-7, p. 160-186, IGI Global, 2010

Laurențiu Ionescu, Alin Mazăre, Gheorghe Șerban, Gabriel Iana, Petre Angheliescu, „Aplicații cu sisteme în timp real”, Editura Matrix ROM, București, ISBN 978-973-755-611-0, 2010, 193 p.

5. Articole/studii publicate în reviste din fluxul științific internațional principal

Laurentiu Ionescu, Alin Mazare, Gheorghe Serban, Ionel Bostan, Step down evolvable power regulator, University of Pitesti – Electronics and computers science, Scientific bulletin, No. 8, Vol.2, 2008, ISSN – 1453 – 1119, Pag 93-98, 2008

Laurentiu-Mihai Ionescu, Constantin Anton, Ion Tutanescu, Alin Mazare, Gheorghe Serban, „Hardware Implementation of BCH ErrorCorrecting Codes on a FPGA”, International Journal of Intelligent Computing Research (IJICR), Volume 1, Issue 3, ISSN 2042 4655, September 2010, Infonomics Society, p. 148-153

Constantin Anton, Laurentiu Mihai Ionescu, Ion Tutanescu, Alin Mazare, Gheorghe Serban, „Implementation and Analysis of an Error Detection and Correction System on FPGA”, International Journal of Intelligent Computing Research (IJICR), Volume 3, Issues 3/4, Sep/Dec 2012, ISSN 2042 46

Laurentiu Ionescu, Alin Mazare, Gheorghe Serban, „Intrinsic Evolvable Hardware Used for Fault Tolerance Systems”, International Journal of Organizational and Collective Intelligence , IGI Global, Vol. 3, Issue 2, Article 2, (pp.43-80), 3(2), 43-80, April-June 2012, DOI: 10.4018/joci.2012040102, DOI: 10.4018/IJOCI, ISSN: 1947-9344, EISSN: 1947-9352, 201255

6. Publicații apărute în lucrări ale principalelor conferințe internaționale de specialitate

Emil Sofron, Gheorghe Șerban, Ionel Bostan, Laurențiu Ionescu, Valeriu Ionescu, Alin Mazare – “Neuron implementation using reconfigurable hardware”, 9th International Conference on Optimisation of Electrical and Electronic Equipments, The Transilvania University, Brasov, pag. 189., ISBN 973-635-285-4, 2004

Nicu Bizon, Alin Mazare, Laurentiu Ionescu, Mihai Oproescu, Marian Raducu, „ Fuzzy control of the DC-DC converter used as power interface for a fuel cell”, The 13th International ICIT Conference Progress in Cryogenics and Isotopes Separation. Proceedings, ISSN 1582-2575, p. 167-176, 2007

Anton C., Ionescu L., Tutanescu I., Mazare A., Serban G., “FPGA-Implemented CRC Algorithm”, International Conference “Applied Electronics”, Pilsen 9-10 September 2009, p. 25-30, ISSN 1803-7232, ISBN 978-80-7043-781-0, IEEE Catalog Number CFP0969A-PRT, University of West Bohemia, Pilsen, Czech Republic, 2009, p.25-29

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C. Anton, L. Ionescu, I. Tutănescu, A. Mazăre, Gh. Șerban, A. Oprea – “Using Associative Memories in Coding Theory for Communication Channels”, World Congress on Internet Security WorldCIS-2012, 10 - 12 June 2012, University of Guelph, Ontario, Canada, published in Conference Proceedings ISBN 978-1-908320-04-9, 2012

G. Șerban, C. Anton, L. Ionescu, I. Tutănescu, A. Mazăre – “H-ARQ System for real time Error Detection and Correction based on Hardware Associative Memories”, International Conference on Applied Electronics AE'2012, 5-7 September, 2012, Pilzen, Czech Republic, lucrare publicata in Conference Proceedings ISSN 1803-7232

I. Tutănescu, C. Anton, L. Ionescu, G. Șerban, A. Mazăre – “Hybrid Error Detecting and Correcting System using Hardware Associative Memories”, NATO International Conference on Military Communications and Information Systems MCC'2012, 8 - 9 October 2012, Gdansk, Poland, ISBN 978-839201209-2

Anton Constantin, Ionescu Laurentiu, Tutanescu Ion, Mazare Alin, Serban Gheorghe, Oprea Adriana, „An Error Detection and Correction System based on Associative Memories”, 2012 20th Telecommunications Forum (TELFOR), ISBN: 978-1-4673-2984-2, p.568-571, Belgrade, Serbia, November, 20-22, 2012

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Ionescu, Laurentiu; Ionita, Silviu; Mazare, Alin, „Embedded System for 3-Axis Acceleration Measuring and Local Computing of Related Speeds and Positions”, 12th IFAC Conference on Programmable Devices and Embedded Systems (2013), Programmable Devices and Embedded Systems, VSB Technical University of Ostrava, Velké Karlovice, Czech Republic, Volume # 12 | Part# 1, 155-160 ISBN: 978-3-902823-53-3, ISSN: 1474-6670, 2013

Barbu V. ,Ionescu L. ,Mazare A. ,Serban G., „Evolvable Hardware Used in Heliostatic Field Tracker System Control”, 2013 International Conference on Applied Electronics, Pilsen 10 – 12 September 2013, 29-33, ISSN 1803-7232 (Print) ISSN 1805-9597 (Online), ISBN 978-80-261-0166-6

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7. Alte lucrări și contribuții științifice

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Fascicula Electrotehnica, Secțiunea Știința Sistemelor și Sisteme de Control, ISSN 1223-2106, pp. 53-56, 2005

Laurentiu Ionescu, Ionel Bostan, Alin Mazare, Valeriu Ionescu, Gheorghe Serban – “The implementation of a command and control machine on evolved hardware based structure”, Proceedings of the 1st

International Conference on Electronics, Computers and Artificial Intelligence, ECAI 2005, ISSN – 1453 – 1119, p.36-39,2005

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Alin Mazare, Laurentiu Ionescu, Ionel Bostan, Silviu Ionita – “Wireless solutions for accelerometer data transmission”, Advances in Intelligent Systems and Technologies, Proceedings ECIT2006 - European Conference on Intelligent Systems and Technologies, Iasi 2006

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